

## AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An apparatus, comprising:
  - a buffer having a trigger, integrated on a component ~~connected~~coupled with a simultaneous bi-directional (SBD) memory bus having ternary logic levels, ~~the trigger is~~ to facilitate observing and echoing of one or more of a plurality of signals transmitted on ~~said~~the SBD memory bus, wherein the trigger operates to instruct the buffer using one or more of ~~the following:~~ a control signal-based indication, an address signal-based indication, and a time-based indication; and
    - a diagnostic device coupled with the buffer, the diagnostic device to facilitate one or more of detecting, accessing, and reading of the plurality of echoed signals.
2. (Currently Amended) The apparatus ~~as in~~of claim 1, further comprising:
  - an observability port coupled with ~~said~~the buffer, ~~the~~ observability port to receive ~~said~~the echoed signals[[],]; and
    - an observability bus ~~connected~~coupled with ~~said~~the observability port, and~~a diagnostic device being at least one of a logic analyzer and a bus analyzer connected with said observability bus and performing at least one of detecting said echoed signals, accessing said echoed signals and reading said echoed signals.~~
3. (Currently Amended) The apparatus ~~as in~~of claim 2, wherein ~~said~~the observability port ~~is~~comprises a logic observability port.
- 4-5. (Cancelled)
6. (Currently Amended) A method, comprising:

transmitting a plurality of signals on a simultaneous bi-directional (SBD) memory

bus having ternary logic levels;

a buffer having a trigger, integrated on a component ~~connected-coupled~~ with the

bus, ~~to facilitate~~facilitating observing and echoing of one or more of a plurality of signals transmitted on the bus, wherein the trigger operates to instruct the buffer using one or more of ~~the following:~~ a control signal-based indication, an address signal-based indication, and a time-based indication; and

a diagnostic device coupled with the buffer, the diagnostic device facilitating one or more of detecting, accessing, and reading of the plurality of echoed signals.

7. (Currently Amended) The method ~~as in~~of claim 6, further comprising[:]  
receiving ~~said~~the plurality of echoed signals; and  
~~performing at least one of detecting said echoed signals, accessing said echoed signals and reading said echoed signals.~~

- 8-13. (Cancelled)

14. (Currently Amended) A system, comprising:  
a memory;  
an input/output (I/O) port;  
a microprocessor; and  
a buffer, having a trigger, integrated on a component coupled with a simultaneous bi-directional (SBD) memory bus having ternary logic levels, the trigger is to facilitate observing and echoing of a plurality of signals transmitted on ~~said~~the bus, wherein the trigger operates to instruct the buffer using one or

- more of the following: a control signal-based indication, an address signal-based indication, and a time-based indication; and
- a diagnostic device coupled with the buffer, the diagnostic device to facilitate one or more of detecting, accessing, and reading of the plurality of echoed signals.
15. (Currently Amended) The system as in of claim 14, further comprising:  
an observability port coupled with said the buffer, the observability port to receive  
said the echoed signals[[],]; and  
an observability bus connected coupled with said the observability port, and a  
diagnostic device being at least one of a logic analyzer and a bus analyzer  
connected with said observability bus and performing at least one of  
detecting said echoed signals, accessing said echoed signals and reading  
said echoed signals.
16. (Currently Amended) The system as in of claim 15, wherein said the observability port is comprises a logic observability port.
- 17-18. (Cancelled)
19. (New) The method of claim 7, wherein the receiving of the echoed signals is performed an observability port.
20. (New) A machine-readable medium having data stored thereon representing sets of instructions which, when executed by a machine, cause the machine to:  
transmit a plurality of signals on a simultaneous bi-directional (SBD) memory bus having  
ternary logic levels;  
facilitate observing and echoing of one or more of a plurality of signals transmitted on the  
bus; and

facilitate one or more of detecting, accessing, and reading of the plurality of echoed signals.

21. (New) The machine-readable medium of claim 20, wherein the sets of instructions, when executed by the machine, further cause the machine to receive the plurality of echoed signals.
22. (New) The machine-readable medium of claim 21, wherein the receiving of the echoed signals is performed an observability port.